

Integrated View The Delphion

Other Views:

INPADOC

JP61172358A2: INTEGRATED CIRCUIT UTILIZING PELTIER COOLING METHOD Title:

JP Japan Country:

Kind:

MASUBUCHI HIDEO Inventor(s):

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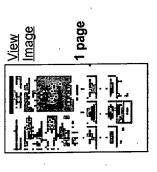
JP1985000013206 Application Number:

H01L 23/38; IPC Class: Jan. 26, 1985 JP1985000013206 Priority Number(s):

Abstract:

affecting the operation of the IC even though the integration degree Purpose: To enable the fellow individual IC substrates to house in of the IC itself is enhanced by a method wherein a cooling method conventional ones, and at the same time, to prevent heat from the case body at intervals one-severalth narrower than the according to the Peltier effect is utilized.

Constitution: This integrated circuit is formed in such a constitution generating parts is made to radiate at the heat radiating part. As the and are formed in such a way that the heat generating parts only of individual circuit main bodies 2 and is such a place as to come into the individual IC circuits protrude thereto; and the heat of the heat contact to the interior of an element 1 or the IC element; the heat plates, which are about 1cm away from the main body substrate radiating part is made to locate on the upper side of the partition hat a Peltier cooling is generated at a place, where is near



cooling is one to cool locally the necessary parts in the IC, the inferiority of efficiency of the thermoelectric cooling method does not exert effect so much to the cooling effect. Moreover, when the parts other than the IC circuits are made in a heat-resisting manner, the partition plates, the fans and the heat pipes can be omitted and even when the temperature in the interior of the element rises higher than the normal one, the IC can be made to operate without being subjected to the effect functionally.

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Family: Show known family members

Other Abstract Info: DERABS G86-242635 DERG86-242635

Foreign References: No patents reference this one

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